1. The following shows some of instructions defined in the Intel IA-32 instruction set architecture. Among the listed instructions, which are protected instructions that cannot be executed in the user mode? Choose all that apply. (20 points, Do not take guesses. Each wrong answer will be penalized.)

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
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<tbody>
<tr>
<td>HLT:</td>
<td>stops instruction execution and places the processor in a HALT state.</td>
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<tr>
<td>ADD:</td>
<td>adds the first operand and the second operand and stores the result in the destination operand.</td>
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<tr>
<td>CALL:</td>
<td>saves procedure linking information on the stack and branches to the procedure specified with the destination operand.</td>
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<tr>
<td>INT:</td>
<td>generates a call to the interrupt or exception handler specified with the destination operand.</td>
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<tr>
<td>POP:</td>
<td>loads the value from the top of the stack to the location specified with the destination operand and then increments the stack pointer.</td>
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<tr>
<td>OUT:</td>
<td>copies the value from the second operand to the I/O port specified with the destination operand.</td>
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<tr>
<td>JMP:</td>
<td>transfers program control to a different point in the instruction stream without recording return information.</td>
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<tr>
<td>CPUID:</td>
<td>provides processor identification information in registers.</td>
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<tr>
<td>CMP:</td>
<td>compares the first source operand with the second source operand and sets the status flags according to the results.</td>
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<tr>
<td>CLI:</td>
<td>clears the interrupt flag, which causes the processor to ignore maskable external interrupts.</td>
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<tr>
<td>MOVSW:</td>
<td>moves the word specified with the second operand to the location specified with the first operand.</td>
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<tr>
<td>SAL:</td>
<td>arithmetically shifts the bits in the destination operand to the left.</td>
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</table>
2. The following code fragment shows the Peterson’s algorithm to solve the critical-section problem between two processes. Suppose we exchange two lines, \texttt{S0} and \texttt{S1}, in the original Peterson’s algorithm, by moving the sentence \texttt{S1} above the sentence \texttt{S0}. Does this modified algorithm still guarantee mutual exclusion of the critical section? If your answer is yes, prove that the algorithm still satisfies the mutual exclusion requirement. Otherwise, give a counter example, in which the algorithm fails. (20 points)

```c
int turn;
int interested[2];
void acquire (int me) {
    int other = 1 - me;
    \texttt{S0:} interested[me] = 1;
    \texttt{S1:} turn = other;
    while (interested[other] && turn == other);
} 
void release (int me) {
    interested[me] = 0;
}
```

3. All of the following operations disrupt the CPU’s sequential flow of instruction execution.

A. Procedure call  
B. Kernel-level thread switching  
C. User-level thread switching  
D. Unconditional branch (jump) instruction

Sort out the four operations in terms of increasing cost to perform. (10 points, no partial points)

\[ A < B < C < D \]
4. Show whether each of the following functions, `sum0()` - `sum3()`, is multithread-safe or not. Explain why? (20 points)

(1) `int sum0 (int n, int a) { int i; for (i = 0; i < a; i++) n += a; return n; }`

(2) `int sum1 (int a) { int n = 0; int i; for (i = 0; i < a; i++) n += a; return n; }`

(3) `int n = 0; int sum2 (int a) { int i; for (i = 0; i < a; i++) n += a; return n; }`

(4) `int sum3 (int a) { static int n = 0; int i; for (i = 0; i < a; i++) n += a; return n; }`
5. [Unisex bathroom problem] A single bathroom is used as a unisex bathroom. The following constraints must be maintained:

(1) If the bathroom is vacant, any man or woman can enter.
(2) Any number of men can be in the bathroom at the same time.
(3) Any number of women can be in the bathroom at the same time.
(4) There cannot be men and women in the bathroom at the same time.

Now we would like to solve this synchronization problem using semaphores. Suppose we have two kinds of threads, men threads and women threads. Men threads call men_enter() when they try to use the bathroom and call men_leave() when they exit. Similarly, women threads call women_enter() and women_leave() when they enter and exit, respectively. According to the above constraints, men threads should be blocked in men_enter() if the bathroom is already occupied by women, and vice versa. Complete the following functions using the semaphore interface provided by Pintos. (30 points)

```c
/* global variables */

/* initialization function */
void init()
{

}

void men_enter()
{

}

void men_leave()
{

}

void women_enter()
{

}

void women_leave()
{

}

/* Semaphore-related functions available in Pintos: */
void sema_init (struct semaphore *sema, unsigned value);
void sema_down (struct semaphore *sema); // wait()
void sema_up (struct semaphore *sema); // signal()
```
The following code is executed on Pintos. Assume that the priority scheduling and the priority donation are already implemented in the Pintos kernel as suggested in the first project assignment. What is the output generated as a result of executing the function foo()? Explain why. (20 points)

NOTE: In Pintos, lower priority values mean lower priorities.